

ABSTRACT OF THE DISCLOSURE

[0028] A row driver according to an embodiment drives a shared row-reset bus to one of a select voltage and a boosted voltage to select a row for readout and to reset the pixels in the row, respectively. The boosted voltage is higher than the select voltage. A row select circuit includes PMOS transistors through which a path may be opened between a supply line carrying the select voltage and the bus. A reset enable circuit includes PMOS transistors through which a path may be opened between a supply line carrying the boosted voltage and the bus. In order to prevent a parasitic diode leakage between the two supply lines during reset, the n-wells of the PMOS transistors in the row select circuit may be coupled to the supply line carrying the boost voltage.